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## NOTES:

An addition has been made to the project. While the 'Clock', 'Stopwatch', and 'Count Down Timer' work exactly as expected (nothing has been changed regarding these modes; the buttons still affect the timer in the expected manner), a new feature has been implemented in the form of a completely new and fun mode.

Have you ever looked at your clock and just wondered to yourself "I really want to gamble. I have an insatiable thirst to get the dopamine rush of gambling." Well now you can with the new 'Gambling' mode. Upon initialisation, you are in 'Clock' mode. When KEY3 is pressed, you will be put into 'Stopwatch' mode, another press and 'CountDownTimer' mode. A final third press of KEY3 will place you into 'Gambling' mode. LEDs 6 to 9 also correspond to the different modes.

As to how the 'Gambling' mode actually works, upon entering this mode. You will see the display showing 00:00:00. Now this is representative of *Your bet:00:Your tokens*. Pressing the reset button (KEY2) will instantly change the display to 00:00:25. This can be pressed at any time throughout the playing process in order to reset and gamble anew (like if you lost all your tokens). The 25 is representative of the number of tokens you currently have (you start with 25). Now KEY1 is connected to a FastAdvance module. As it is pressed or held, the number of tokens you have will decrease (and thus the number on the right two seven segment displays will gradually decrease). You will notice the number on the left two seven segment displays begin to increase at an equal rate. This is representative of how many tokens you are betting. After deciding how many tokens you are willing to bet, press KEY0 to start the gambling process. Now LED3 indicates that you gambled and will turn on when KEY0 is pressed and off when you release the button. LED2 indicates the outcome of your gamble. If it turns on, this means your gamble was successful and if it is off, this means your gamble was not successful. There is roughly a 50/50 chance of winning or losing any gamble in this game. If you win the gamble, you will notice that the number of tokens (the number on the 2 far right seven segment displays) will be the number of tokens you had previously, now added with the number of tokens you bet while the number displayed in the far left seven segment displays will be returned back to 00 and the game continues, this time you have more tokens then you had previously. This way you gain more if you bet more. Now if you lose the gamble, you will notice that the number of tokens (the number on the 2 far right seven segment displays) will not change at all, ie. you lost the number of tokens you bet. The number displayed in the far left seven segment displays will be returned back to 00 and the game continues, this time you have less tokens then you had previously. This way you'll be worried about betting a large number of tokens. It is not possible to bet more tokens than you currently have, it won't let you, and you can only increase your bet. Once you have placed down a token to bet, you can't take it back. I'd make it so you could but there are not enough buttons on the board to allow for this functionality. The middle two seven segment displays serve no purpose in this game.

Here is an example (You start with 25 tokens so the initial display will be 00:00:25):

| Previous Display | Bet | Current Display | Outcome | New Display |
|------------------|-----|-----------------|---------|-------------|
| 00:00:25         | 5   | 05:00:20        | Win     | 00:00:30    |
| 00:00:30         | 15  | 15:00:15        | Win     | 00:00:45    |
| 00:00:45         | 20  | 20:00:25        | Lose    | 00:00:25    |
| 00:00:25         | 3   | 03:00:22        | Win     | 00:00:28    |
| 00:00:28         | 16  | 16:00:12        | Lose    | 00:00:12    |
| 00:00:12         | 12  | 12:00:00        | Lose    | 00:00:00    |

Your goal is to acquire as many tokens as you possibly can (maximum of 99). Your other goal is to have fun giving in to your crippling gambling addiction.

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```

1 module Watch(input CLOCK_50, input [2:0] KEY,
2               output [6:0] HEX5, HEX4, HEX3, HEX2, HEX1, HEX0);
3
4     // Time module
5     wire [6:0] secs;
6     wire [6:0] mins;
7     wire [4:0] hrs;
8     Time timer(.clk(CLOCK_50), .edit(edit),
9               .secs(secs), .mins(mins), .hrs(hrs));
10
11     // For button control module
12     wire [3:0] edit;
13     ButtonControl buttoncontrol(.clk(CLOCK_50), .key(KEY), .display(edit));
14
15     // For the flash control
16     wire [3:0] flash_digit;
17     FlashControl flashcontrol(.clk(CLOCK_50), .key(KEY[2]), .led(flash_digit));
18
19     // For the display
20     Display display_secs(.clk(CLOCK_50), .num(secs), .flash(flash_digit[1]),
21                          .sseg_tens(HEX1), .sseg_ones(HEX0));
22     Display display_mins(.clk(CLOCK_50), .num(mins), .flash(flash_digit[2]),
23                          .sseg_tens(HEX3), .sseg_ones(HEX2));
24     Display display_hrs(.clk(CLOCK_50), .num(hrs), .flash(flash_digit[3]),
25                          .sseg_tens(HEX5), .sseg_ones(HEX4));
26 endmodule

```

Figure 22: Top-level module, Watch

```

1 module Time(input clk, input [3:0] edit,
2             output [5:0] secs, mins, output [4:0] hrs);
3     localparam N = 50000000;
4     localparam BW = $clog2(N);
5     wire [BW-1:0] tick;
6
7     // Make it so only the flashing digit can be edited
8     wire editablesecs;
9     wire editablemins;
10    wire editablehrs;
11    // state 00 is seconds, state 10 is minutes and state 11 is hours
12    assign editablesecs = (edit[1:0] == 2'b01) ? 1'b1 : 1'b0;
13    assign editablemins = (edit[1:0] == 2'b10) ? 1'b1 : 1'b0;
14    assign editablehrs = (edit[1:0] == 2'b11) ? 1'b1 : 1'b0;
15
16    Counter #(N-1, .WIDTH(BW)) divider(.clk(clk), .enable(edit[1:0] == 2'b00),
17                                         .plus(edit[3]), .minus(edit[2]), .editable(1'b1), .cnt(tick));
18    Counter #(N-1, .WIDTH(BW)) cs(.clk(clk), .enable(tick == 0),
19                                   .plus(edit[3]), .minus(edit[2]), .editable(editablesecs), .cnt(secs));
20    Counter #(N-1, .WIDTH(BW)) cm(.clk(clk), .enable(secs == 0 && tick == 1),
21                                   .plus(edit[3]), .minus(edit[2]), .editable(editablemins), .cnt(mins));
22    Counter #(N-1, .WIDTH(BW)) ch(.clk(clk), .enable(mins == 0 && secs == 0 && tick == 2),
23                                   .plus(edit[3]), .minus(edit[2]), .editable(editablehrs), .cnt(hrs));
24
25 endmodule

```

Figure 23: Time module

```

1  module Counter
2      #(parameter MAX = 1, WIDTH = 1, UP = 1) (
3          input clk,
4          input enable,
5          input plus,
6          input minus,
7          input editable,
8          output reg [WIDTH - 1:0] cnt
9      );
10
11     initial cnt = 0;
12
13     reg [WIDTH-1:0] next_cnt;
14
15     always @(posedge clk)
16         cnt <= next_cnt;
17
18     always @(*)
19         // If both plus and minus are pressed, do nothing
20         if (plus && minus);
21         // If plus is pressed, increment count
22         else if (plus && !minus && editable)
23             next_cnt = (cnt == MAX) ? 0 : cnt + 1;
24         // If minus is pressed, reduce count
25         else if (minus && !plus && editable)
26             next_cnt = (cnt == 0) ? MAX : cnt - 1;
27         // If neither is pressed and enable is 1
28         else if (enable && !plus && !minus)
29             if (UP)
30                 next_cnt = (cnt == MAX) ? 0 : cnt + 1;
31             else
32                 next_cnt = (cnt == 0) ? MAX : cnt - 1;
33         else
34             next_cnt = cnt;
35
36     endmodule

```

Figure 24: Counter module

```

1  module ButtonControl(input clk, input [2:0] key, output [3:0] display);
2
3      // Mode module
4      Mode mode(.clk(clk), .in(!key[2]), .out(display[1:0]));
5
6      // The following FastAdvance module
7      FastAdvance plus(.clk(clk), .in(!key[1]), .out(display[3]));
8      FastAdvance minus(.clk(clk), .in(!key[0]), .out(display[2]));
9  endmodule

```

Figure 25: ButtonControl module

```

1  module Mode(input clk, in, output reg [1:0] out);
2      localparam normal = 2'b00, secs = 2'b01, mins = 2'b10, hrs = 2'b11;
3
4      reg [25:0] tracker = 0; // Tracks how much time has passed
5      reg enabled = 0; // A flag to check whether a second has passed
6
7      // Current state and next state
8      reg [1:0] state, next_state;
9      initial state = normal;
10
11     // Check that the button has been held for 1 second
12     always @(posedge clk) begin
13         if (in) begin
14             if (tracker < 50_000_000)
15                 tracker <= tracker + 1'b1;
16             else
17                 enabled <= 1'b1;
18         end else begin
19             tracker <= 0;
20             enabled <= 0;
21         end
22     end
23
24     // The following is basically the RisingEdgeDetector module
25     // to detect that the button has been pressed and not held
26     reg prev = 0; // State
27     wire next_prev;
28
29     always @(posedge clk)
30         prev <= next_prev;
31
32     assign next_prev = in;
33
34     wire press;
35     assign press = (!prev && in);
36
37     // Flip flop
38     always @(posedge clk)
39         state <= next_state;
40
41     // Next state logic
42     always @(*) begin
43         case (state)
44             normal: next_state = (enabled && in) ? secs : normal;
45             secs: next_state = (press) ? mins : secs;
46             mins: next_state = (press) ? hrs : mins;
47             hrs: next_state = (press) ? normal : hrs;
48             default: next_state = normal;
49         endcase
50
51         // output logic
52         out = state;
53     end
54 endmodule

```

Figure 26: Mode module

```

1  module FastAdvance
2      #(parameter LONG=25_000_000, PERIOD=5_000_000) (
3          input clk,
4          input in,
5          output out);
6
7      localparam BWL = $clog2(LONG+1);
8      localparam BWP = $clog2(PERIOD);
9      reg [BWL-1:0] cl = 0, next_cl;
10     reg [BWP-1:0] cp = 0, next_cp;
11
12     always @(posedge clk)
13         {cl,cp} <= {next_cl,next_cp};
14
15     // Next state logic
16     always @(*) begin
17         // If the button is being pressed and cl is less than the number of
18         // clock cycles to wait before transitioning to the fast advance
19         // mode, LONG, then increase cl by 1, otherwise if the button is
20         // not pressed, set cl back to 0 and if it is, keep cl as what it
21         // currently is.
22         if (in == 1 && cl < LONG)
23             next_cl = cl + 1;
24         else
25             if (in == 0)
26                 next_cl = 0;
27             else
28                 next_cl = cl;
29         // If the button is being pressed and cl is now equal to the number of
30         // clock cycles to wait before transitioning to the fast advance
31         // mode, LONG, and cp is equal to the period, set cp to 0, otherwise
32         // increase cp to 0. If cl has yet to reach LONG, keep cp as 0
33         if (in == 1 && cl == LONG)
34             if (cp == PERIOD - 1)
35                 next_cp = 0;
36             else
37                 next_cp = cp + 1;
38         else
39             next_cp = 0;
40     end
41
42     // While the button is pressed, output a 1 if cl is 0 (the button is
43     // pressed or cl == LONG and cp == 0 (the button is held)
44     assign out = (in == 1 && (cl == 0 || (cl == LONG && cp == 0))) ? 1 : 0;
45 endmodule

```

Figure 27: FastAdvance module

```

1  module FlashControl(input clk, input key, output [3:0] led);
2
3      // Mode module
4      wire [1:0] digit;
5      Mode mode(.clk(clk), .in(!key), .out(digit));
6
7      // DeMux4 module
8      wire [3:0] led_output;
9      DeMUX4 demux4(.in(!key), .sel(digit), .out(led_output));
10
11     // The flashing module
12     wire flash_out;
13     Flash flash(.clk(clk), .out(flash_out));
14
15     // Now make it flash
16     assign led = (flash_out) ? led_output : 4'b0000;
17 endmodule

```

Figure 28: FlashControl module

```

1  module DeMUX4(input in, input [1:0] sel, output [3:0] out);
2      reg [3:0] temp_out;
3
4      always @(*)
5          case(sel)
6              2'b00: temp_out = 4'b0001;
7              2'b01: temp_out = 4'b0010;
8              2'b10: temp_out = 4'b0100;
9              2'b11: temp_out = 4'b1000;
10         endcase
11
12     assign out = temp_out;
13 endmodule

```

Figure 29: DeMUX4 module

```

1  module Flash(input clk, output reg out);
2
3      reg [24:0] counter = 0;
4
5      always @(posedge clk)
6          // 50000000Hz / 25000000 = 2 Hz
7          if (counter < 25_000_000)
8              counter <= counter + 1;
9          else
10             counter <= 0;
11
12     // The duty cycle
13     always @(posedge clk)
14         // 25000000 * 0.8 = 20000000
15         if (counter < 20_000_000)
16             out <= 1;
17         else
18             out <= 0;
19
20 endmodule

```

Figure 30: Flash module

```

1  module Display(input clk, input [6:0] num, input flash,
2      output [6:0] sseg_tens, sseg_ones);
3      wire [3:0] digit_tens;
4      wire [3:0] digit_ones;
5      Binary2Decimal digit(.binary(num), .decimal_tens(digit_tens),
6          .decimal_ones(digit_ones));
7      SSegDecoder digits_tens(.clk(clk), .digit(digit_tens), .flash(flash),
8          .sseg(sseg_tens));
9      SSegDecoder digits_ones(.clk(clk), .digit(digit_ones), .flash(flash),
10         .sseg(sseg_ones));
11 endmodule

```

Figure 31: Display module

```

1 module Binary2Decimal(input [6:0] binary,
2   output [3:0] decimal_tens, decimal_ones);
3   assign decimal_tens = binary / 10;
4   assign decimal_ones = binary % 10;
5 endmodule

```

Figure 32: Binary2Decimal module

```

1 module SSegDecoder(input clk, input [3:0] digit, input flash,
2   output reg [6:0] sseg);
3
4   always @(posedge clk)
5     if (flash)
6       sseg <= 7'b1111111;
7     else
8       case(digit)
9         0: sseg <= 7'b1000000;
10        1: sseg <= 7'b1111001;
11        2: sseg <= 7'b0100100;
12        3: sseg <= 7'b0110000;
13        4: sseg <= 7'b0011001;
14        5: sseg <= 7'b0010010;
15        6: sseg <= 7'b0000010;
16        7: sseg <= 7'b1111000;
17        8: sseg <= 7'b0000000;
18        9: sseg <= 7'b0010000;
19        default: sseg <= 7'b1111111;
20      endcase
21 endmodule

```

Figure 33: SSegDecoder module